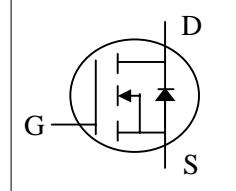




- ▼ Lower Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic

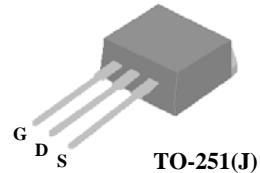
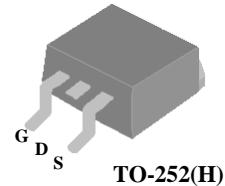


BV_{DSS}	40V
$R_{DS(ON)}$	7mΩ
I_D	75A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-252 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP9468GJ) are available for low-profile applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current ³	75	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	57	A
I_{DM}	Pulsed Drain Current ¹	300	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	89	W
	Linear Derating Factor	0.7	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.4	$^\circ C/W$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	110	$^\circ C/W$

**Electrical Characteristics@T_j=25°C(unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40	-	-	V
Δ BV _{DSS} /Δ T _j	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =250uA	-	0.01	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =45A	-	-	7	mΩ
		V _{GS} =4.5V, I _D =30A	-	-	9	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.5	-	1.5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =30A	-	75	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =40V, V _{GS} =0V	-	-	1	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =32V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =30A	-	36	58	nC
Q _{gs}	Gate-Source Charge	V _{DS} =30V	-	4	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	20	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =20V	-	8	-	ns
t _r	Rise Time	I _D =30A	-	62	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =1.0Ω, V _{GS} =10V	-	36	-	ns
t _f	Fall Time	R _D =0.67Ω	-	16	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	2235	3580	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	365	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	325	-	pF
R _g	Gate Resistance	f=1.0MHz	-	1.8	2.7	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =30A, V _{GS} =0V	-	-	1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =20A, V _{GS} =0V,	-	38	-	ns
Q _{rr}	Reverse Recovery Charge	dl/dt=100A/μs	-	30	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

3.Package limitation current is 75A

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

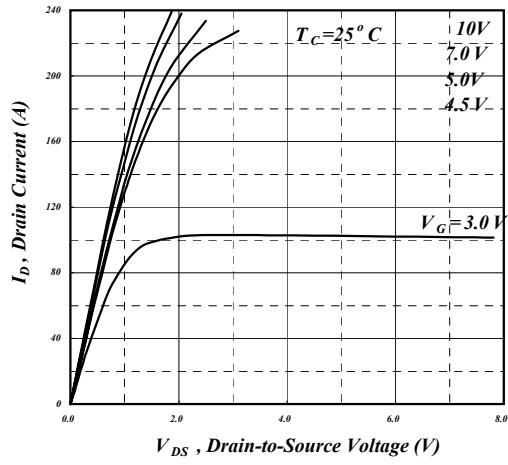


Fig 1. Typical Output Characteristics

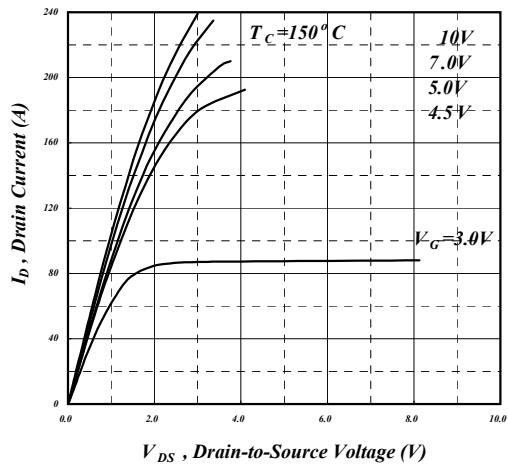


Fig 2. Typical Output Characteristics

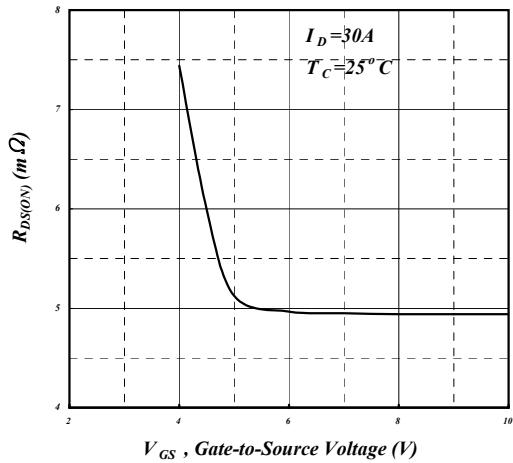


Fig 3. On-Resistance v.s. Gate Voltage

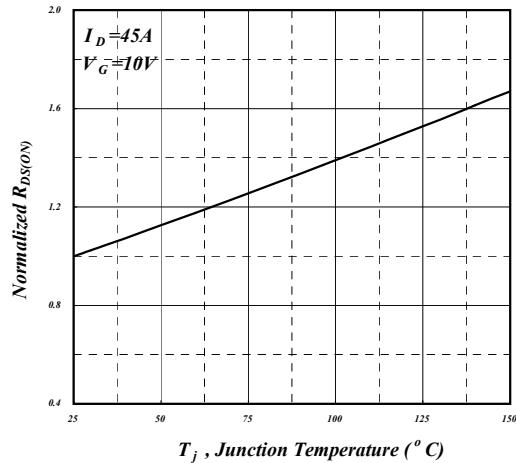


Fig 4. Normalized On-Resistance v.s. Junction Temperature

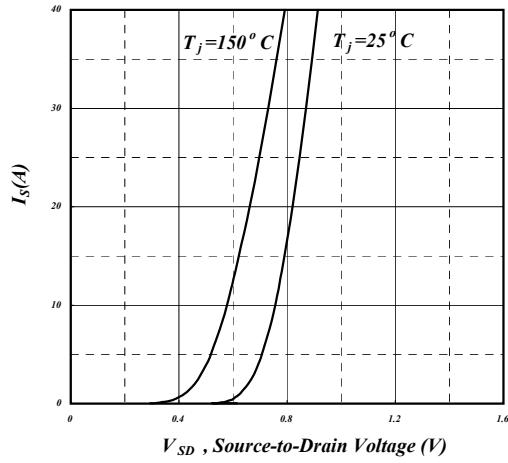


Fig 5. Forward Characteristic of Reverse Diode

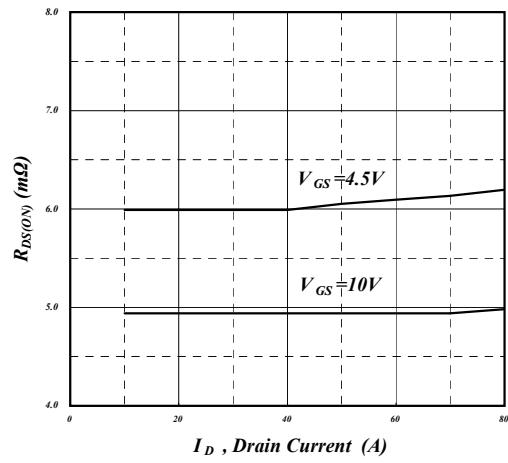
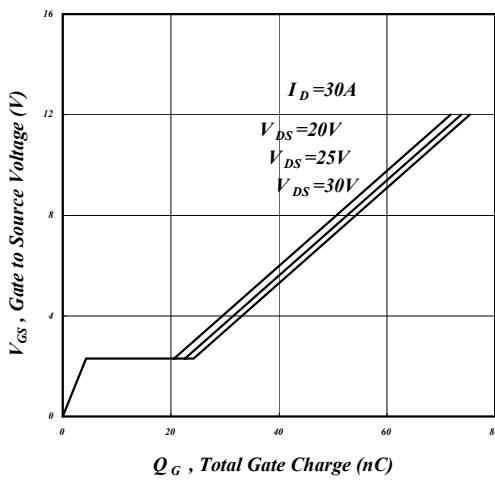
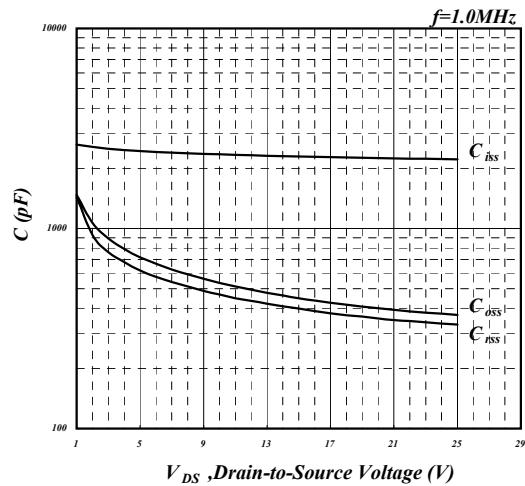
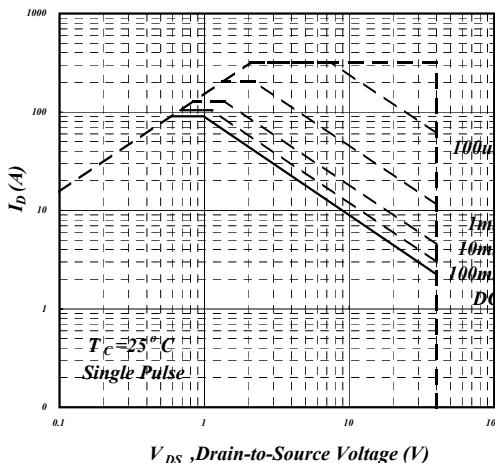
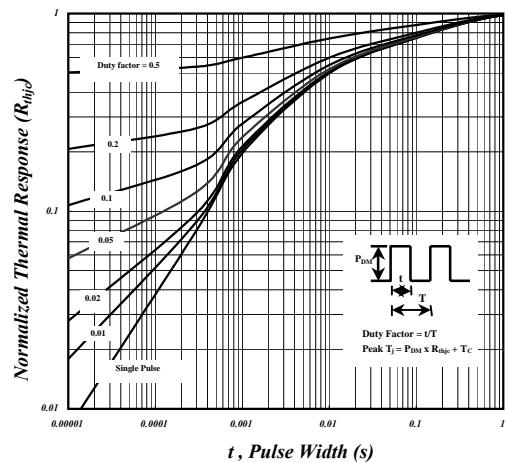
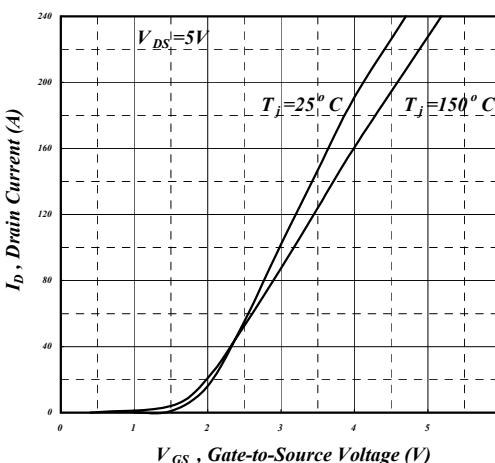
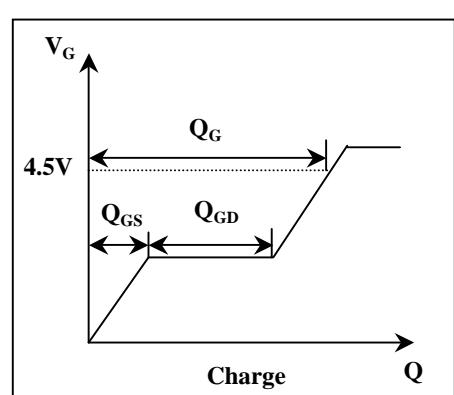
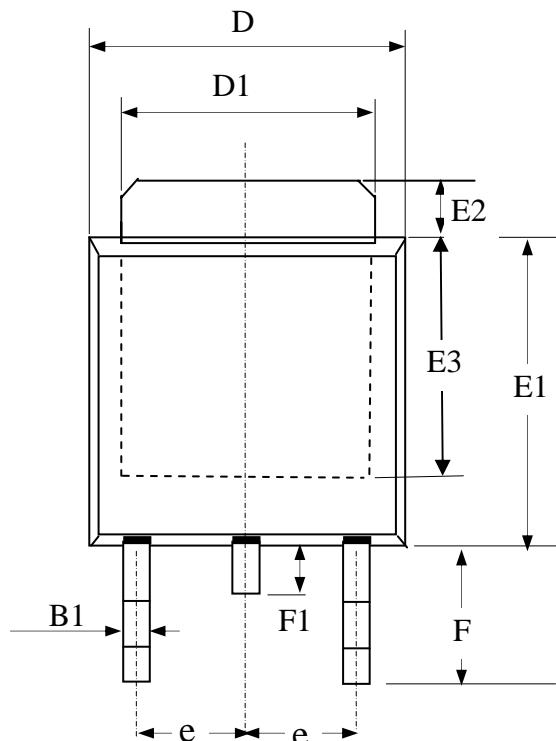


Fig 6. On-Resistance vs. Drain Current


Fig 7. Gate Charge Characteristics

Fig 8. Typical Capacitance Characteristics

Fig 9. Maximum Safe Operating Area

Fig 10. Effective Transient Thermal Impedance

Fig 11. Transfer Characteristics

Fig 12. Gate Charge Waveform



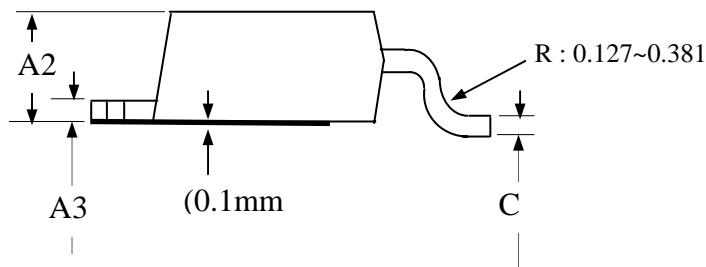
Package Outline : TO-252



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.50	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

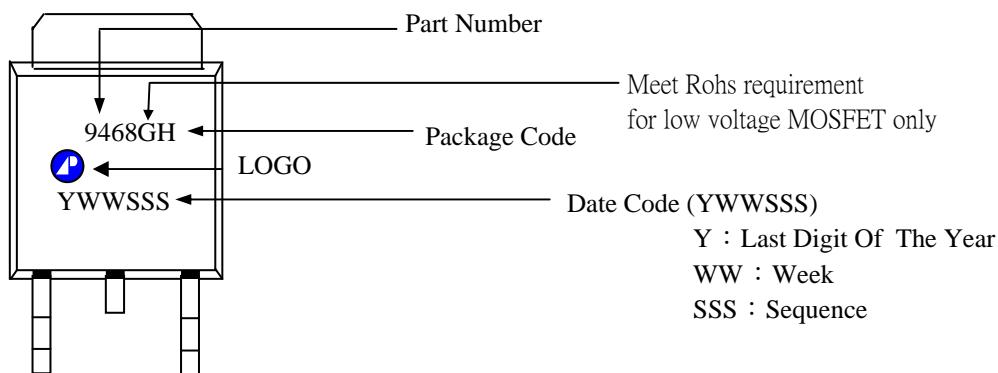
1. All Dimensions Are in Millimeters.

2. Dimension Does Not Include Mold Protrusions.



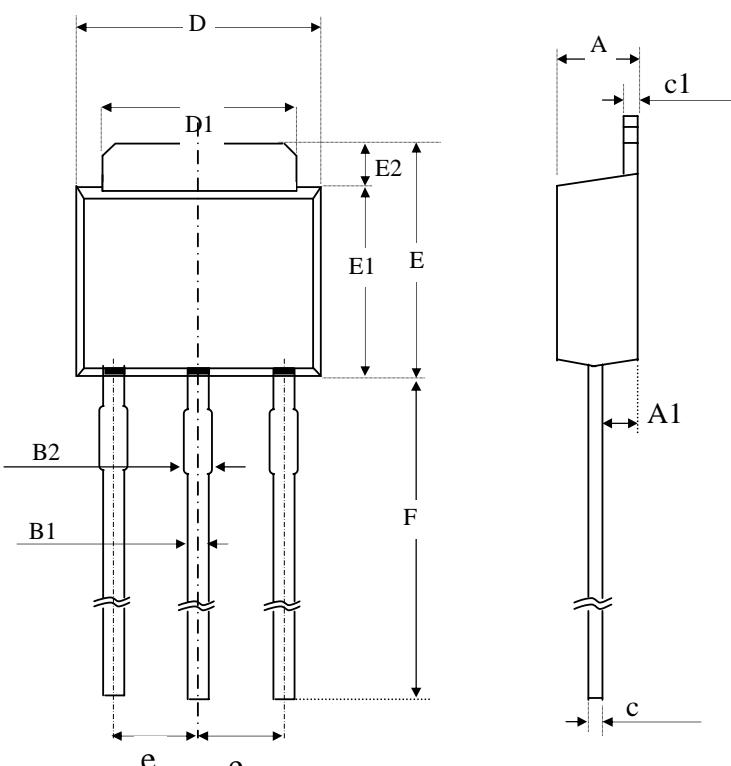
Part Marking Information & Packing : TO-252

Laser Marking





Package Outline : TO-251

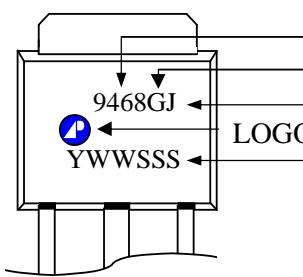


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.90	1.20	1.50
B1	0.40	0.60	0.80
B2	0.60	0.85	1.05
c	0.40	0.50	0.60
c1	0.40	0.50	0.60
D	6.40	6.60	6.80
D1	4.80	5.20	5.50
E	6.70	7.00	7.30
E1	5.40	5.60	5.80
E2	1.30	1.50	1.70
e	----	2.30	----
F	7.00	8.30	9.60

1. All Dimensions Are in Millimeters.

2. Dimension Does Not Include Mold Protrusions.

Part Marking Information & Packing : TO-251



Part Number

meet Rohs requirement
for low voltage MOSFET only

9468GJ
LOGO

Package Code

Date Code (YWWSSS)

Y : Last Digit Of The Year

WW : Week

SSS : Sequence