

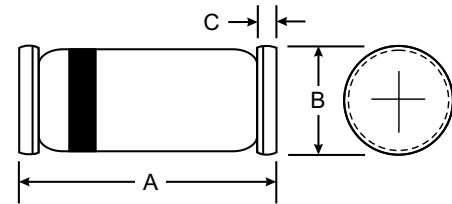
VOLTAGE RANGE: 50 V
CURRENT: 0.2 A

Features

- For general purpose applications
- This diode features very low turn-on voltage and fast switching. These devices are protected by a PN junction guard ring against excessive voltage, such as electrostatic discharges

Mechanical Data

- Case: SOD-80/LL34, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.05 grams (approx.)



LL34/ SOD-80		
Dim	Min	Max
A	3.30	3.70
B	1.30	1.60
C	0.28	0.50
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Continuous reverse voltage	V_R	50	V
Forward continuous current @ $T_A=25^\circ\text{C}$	I_F	200 ¹⁾	mA
Peak forward current @ $T_A=25^\circ\text{C}$	I_{FM}	500 ¹⁾	mA
Surge forward current @ $t_p<1\text{s}, T_A=25^\circ\text{C}$	I_{FSM}	5 ¹⁾	A
Power dissipation @ $T_A=65^\circ\text{C}$	P_{tot}	200 ¹⁾	mW
Junction temperature	T_J	125	°C
Ambient operating temperature range	T_A	-55 ---- 125	°C
Storage temperature range	T_{STG}	-55 ---- 150	°C

1) Valid provided that leads at a distance of 4mm from case are kept at ambient temperature

Characteristic	Symbol	Typ.	Max.	Unit
Reverse breakdown voltage	V_R	50.0		V
Forward voltage Pulse test $t_p<300\ \mu\text{s}, \delta<2\%$ @ $I_F=0.1\text{mA}$ @ $I_F=1\text{mA}$ @ $I_F=10\text{mA}$ @ $I_F=30\text{mA}$ @ $I_F=100\text{mA}$	V_F		0.30 0.38 0.45 0.60 0.90	V V V V V
Leakage current $V_R=40\text{V}$	I_R		5.0	μA
Diode capacitance at $V_R=1\text{V}, f=1\text{MHz}$	C_d		8	pF
Reverse recovery time @ $I_F=10\text{mA}, I_R=10\text{mA}, I_R=1\text{mA}$	t_{rr}		5	ns
Thermal resistance junction to ambient	$R_{\theta JA}$		430 ¹⁾	°C/W

1) Valid provided that leads at a distance of 4mm from case are kept at ambient temperature



FIG.1 – ADMISSIBLE POWER DISSIPATION VS. AMBIENT TEMPERATURE

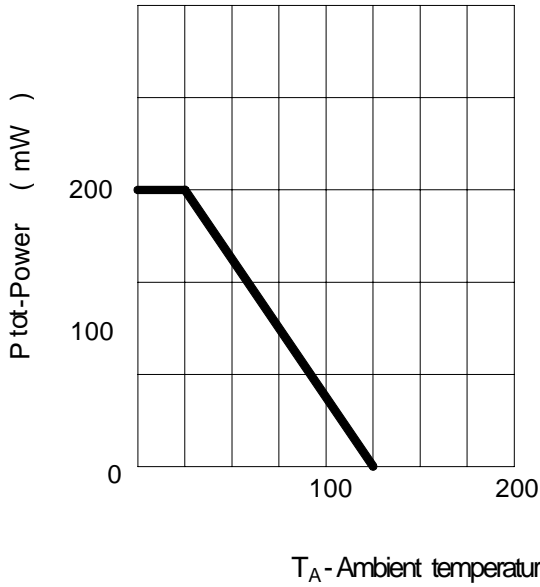


FIG. 2 – TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

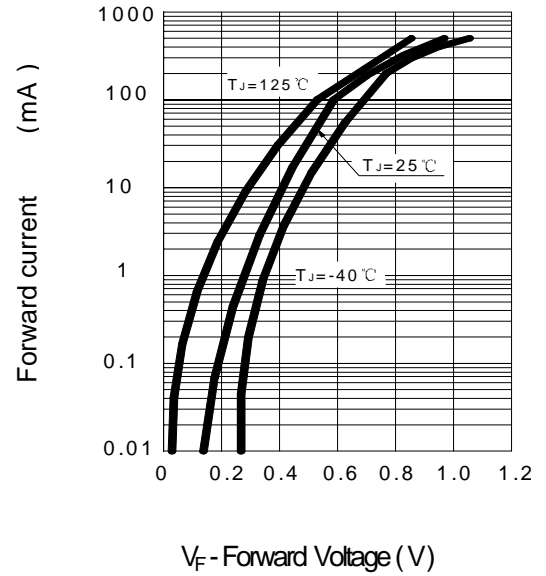


FIG. 3 – TYPICAL REVERSE CHARACTERISTICS

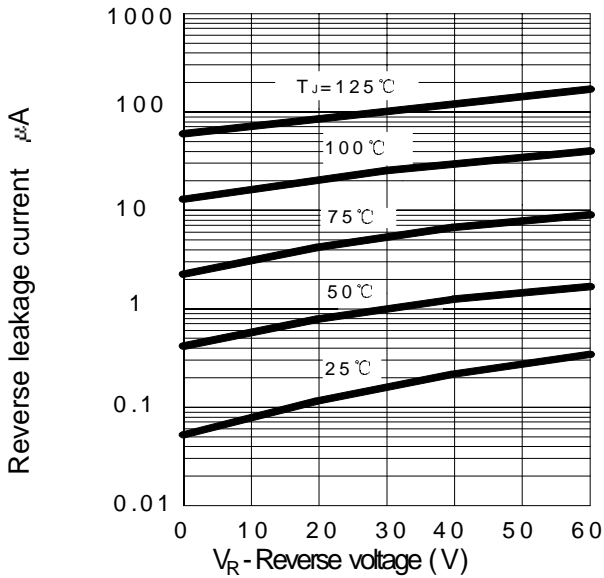


FIG.4 – TYPICAL JUNCTION CAPACITANCE

