

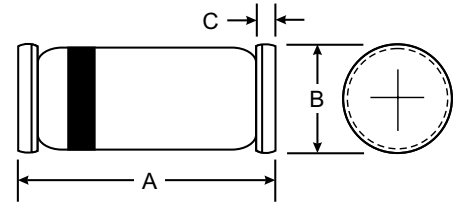


Features

- Fast Switching Speed
- General Purpose Rectification
- Silicon Epitaxial Planar Construction

Mechanical Data

- Case: SOD-80/LL34, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.05 grams (approx.)



LL34/ SOD-80		
Dim	Min	Max
A	3.30	3.70
B	1.30	1.60
C	0.28	0.50
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	FDLL914A	Unit
Non-Repetitive Peak Reverse Voltage	V _{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	75	V
RMS Reverse Voltage	V _{R(RMS)}	53	V
Forward Continuous Current (Note 1)	I _{FM}	300	mA
Average Rectified Output Current (Note 1)	I _O	150	mA
Non-Repetitive Peak Forward Surge Current @ t = 1.0s @ t = 1.0μs	I _{FSM}	1.0 2.0	A
Power Dissipation (Note 1) Derate Above 25°C	P _d	500 1.68	mW mW/°C
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	300	K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +175	°C

Characteristic	Symbol	Min	Max	Unit	Test Condition
Maximum Forward Voltage		—	1.0		I _F = 10mA
Maximum Peak Reverse Current	I _{RM}	—	5.0 50 30 25	μA μA μA nA	V _R = 75V V _R = 70V, T _j = 150°C V _R = 20V, T _j = 150°C V _R = 20V
Capacitance	C _j	—	4.0	pF	V _R = 0, f = 1.0MHz
Reverse Recovery Time	t _{rr}	—	4.0	ns	I _F = 10mA to I _R = 1.0mA V _R = 6.0V, R _L = 100Ω

Notes: 1. Valid provided that device terminals are kept at ambient temperature.

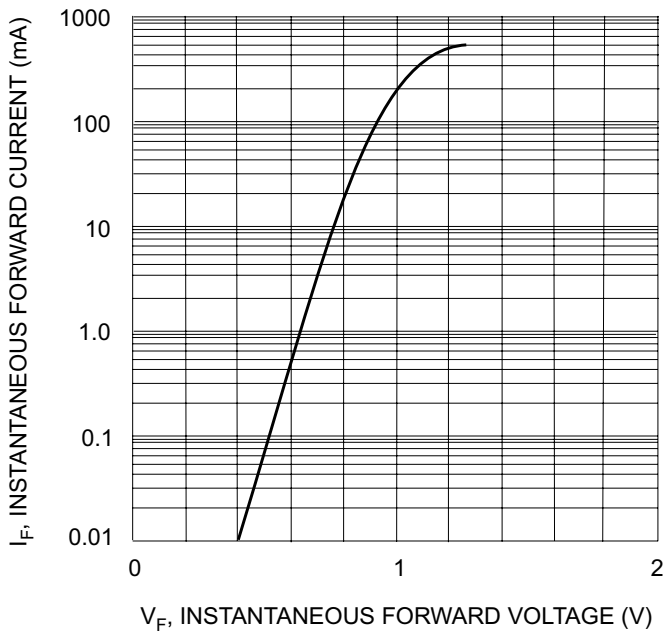


Fig. 1 Forward Characteristics

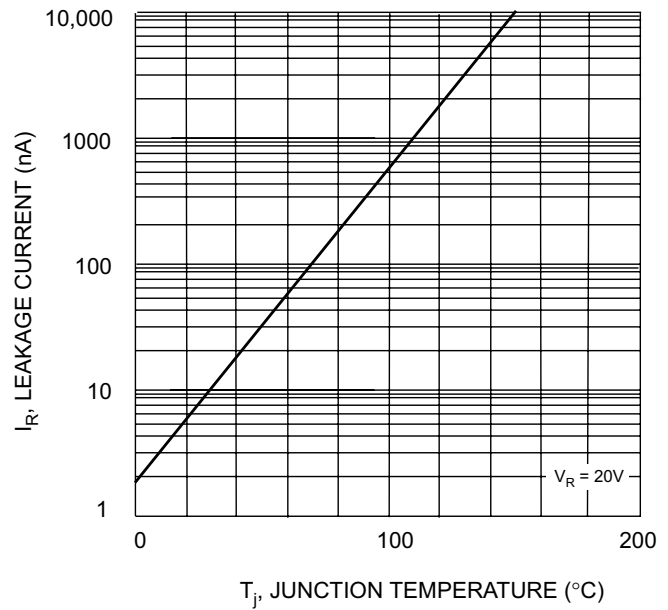


Fig. 2, Leakage Current vs Junction Temperature