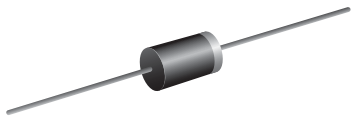
# EM520

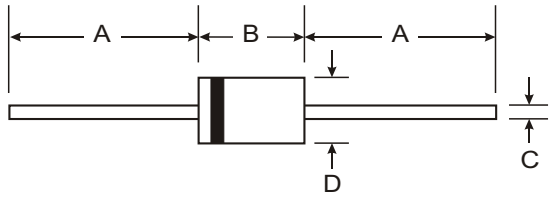
## AXIAL LEADED RECTIFIER DIODES

**VOLTAGE RANGE: 2000V**
**CURRENT: 1.0 A**



## Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability




## Mechanical Data

- Case:DO-41 Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.35 grams (approx.)
- Mounting Position: Any
- Marking: Type Number

RoHS COMPLIANT

| DO-41 | | |
|---|---|---|
| **Dim** | **Min** | **Max** |
| **A** | 25.40 | — |
| **B** | 4.06 | 5.21 |
| **C** | 0.71 | 0.864 |
| **D** | 2.00 | 2.72 |
| **All Dimensions in mm** | | |

## Maximum Ratings and Electrical Characteristics $T_A$ = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | Symbol | EM520 | Unit |
|---|---|---|---|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage | $V_{RRM}$<br>$V_{RWM}$<br>$V_R$ | 2000 | V |
| RMS Reverse Voltage | $V_{R(RMS)}$ | 1400 | V |
| Average Rectified Output Current (Note 1) @$T_A$ = 75°C | $I_O$ | 1.0 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | $I_{FSM}$ | 30 | A |
| Forward Voltage @$I_F$ = 1.0A | $V_{FM}$ | 1.1 | V |
| Peak Reverse Current @$T_A$ = 25°C<br>At Rated DC Blocking Voltage @$T_A$ = 100°C | $I_{RM}$ | 5.0<br>500 | μA |
| Typical Junction Capacitance (Note 2) | $C_j$ | 15 | pF |
| Typical Thermal Resistance Junction to Ambient (Note 1) | $R_{\theta JA}$ | 50 | K/W |
| Operating Temperature Range | $T_j$ | -65 to +125 | °C |
| Storage Temperature Range | $T_{STG}$ | -65 to +150 | °C |

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case
2. Measured at 1.0 MHz and Applied Reverse Voltage of 4.0V D.C.

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

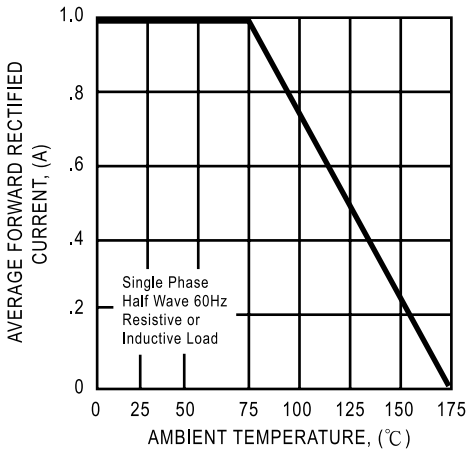



FIG. 2 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

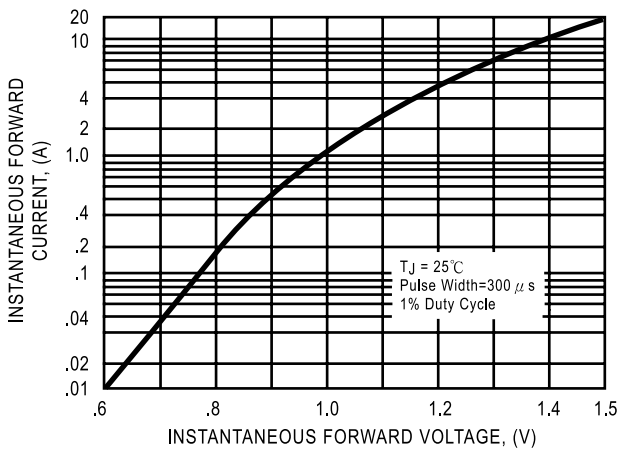



FIG. 3 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

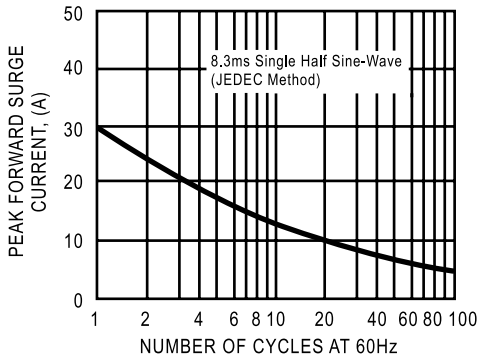



FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

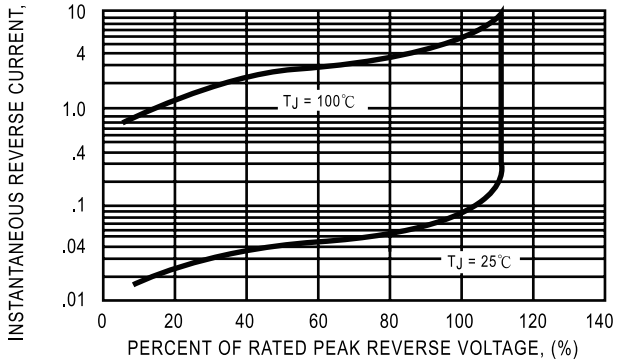



FIG. 5 - TYPICAL JUNCTION CAPACITANCE