

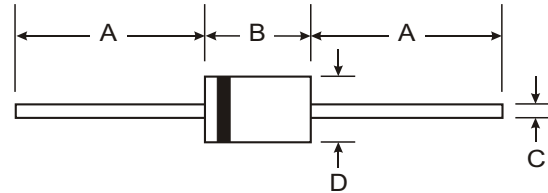
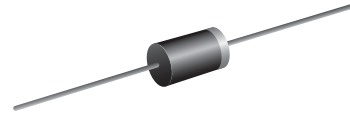
**VOLTAGE RANGE: 50 - 1000V**  
**CURRENT: 1.0 A**

### Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

### Mechanical Data

- Case: DO-41 Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.34 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



DO-41		
Dim	Min	Max
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

### Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

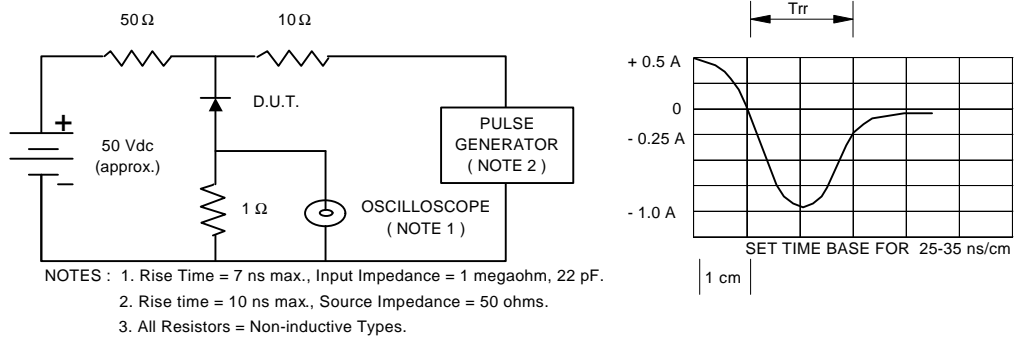
Characteristic	Symbol	HER 101	HER 102	HER 103	HER 104	HER 105	HER 106	HER 107	HER 108	Unit
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	VRMS	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	VDC	50	100	200	300	400	600	800	1000	V
Maximum Average Forward Current 0.375"(9.5mm) Lead Length $T_a = 55^\circ\text{C}$	$I_{F(AV)}$	1.0								A
Maximum Peak Forward Surge Current, 8.3ms Single half sine wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	30								A
Maximum Forward Voltage at $I_F = 1.0\text{ A}$	$V_F$	1.1				1.7				V
Maximum DC Reverse Current $T_a = 25^\circ\text{C}$ at Rated DC Blocking Voltage $T_a = 100^\circ\text{C}$	$I_R$	5								$\mu\text{A}$
	$I_{R(H)}$	50								$\mu\text{A}$
Maximum Reverse Recovery Time ( Note 1 )	$T_{rr}$	50				75				ns
Typical Junction Capacitance ( Note 2 )	$C_J$	50								pf
Junction Temperature Range	$T_J$	- 65 to + 150								$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	- 65 to + 150								$^\circ\text{C}$

#### Notes :

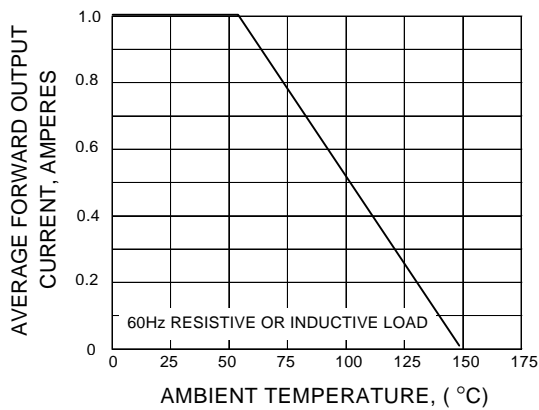
- ( 1 ) Reverse Recovery Test Conditions :  $I_F = 0.5\text{ A}$ ,  $I_R = 1.0\text{ A}$ ,  $I_{rr} = 0.25\text{ A}$ .
- ( 2 ) Measured at 1.0 MHz and applied reverse voltage of 4.0 VDC

## RATING AND CHARACTERISTIC CURVES ( HER101 - HER108 )

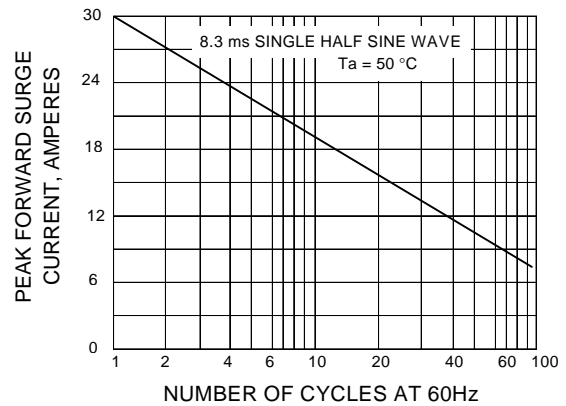
**FIG.1 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM**



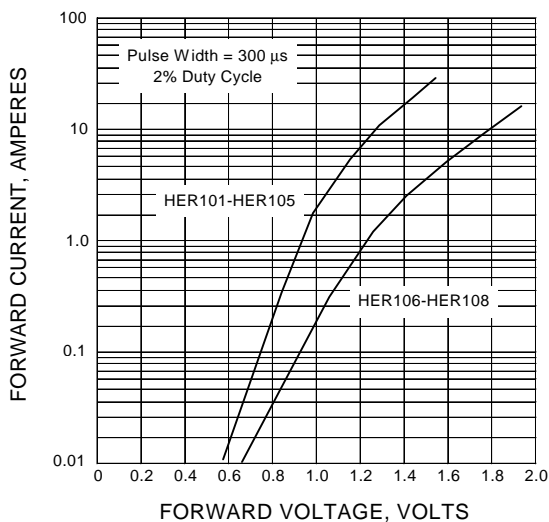
**FIG.2 - DERATING CURVE FOR OUTPUT RECTIFIED CURRENT**



**FIG.3 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT**



**FIG.4 - TYPICAL FORWARD CHARACTERISTICS**



**FIG.5 - TYPICAL REVERSE CHARACTERISTICS**

