

VOLTAGE RANGE: 20 - 40V

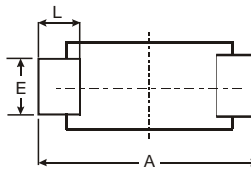
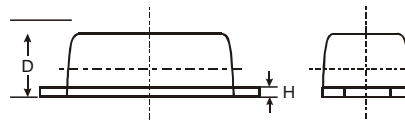
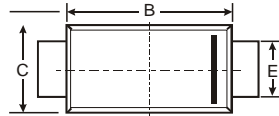
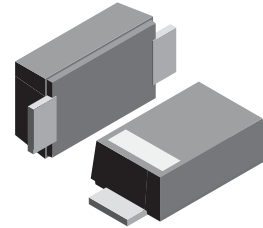
CURRENT: 1.0 A

Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Low Reverse Recovery Time
- Low Reverse Capacitance

Mechanical Data

- Case: SOD-123FL
plastic body over passivated junction
- Terminals : Plated axial leads,
- solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.50	3.80	3.65
B	2.60	2.90	2.75
C	1.70	1.90	1.80
D	0.09	1.10	1.00
E	0.08	1.10	0.095
H	0.12	0.20	0.16
L	0.07	0.09	0.08
All Dimensions in mm			

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SA10QA02	SA10QA03	SA10QA04	Unit
	Marking	A02	A03	A04	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	20	30	40	V
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	V
Average Rectified Output Current (Note 1) @T _L = 90°C	I _O	1.0			A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	25			A
Forward Voltage @I _F = 1.0A	V _{FM}	0.450	0.550	0.60	V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	1.0 10			mA
Typical Junction Capacitance (Note 2)	C _j	110			pF
Typical Thermal Resistance Junction to Lead (Note 1)	R _{θJL}	60			K/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150			°C

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

RATINGS AND CHARACTERISTIC CURVES SA10QA02 THRU SA10QA04

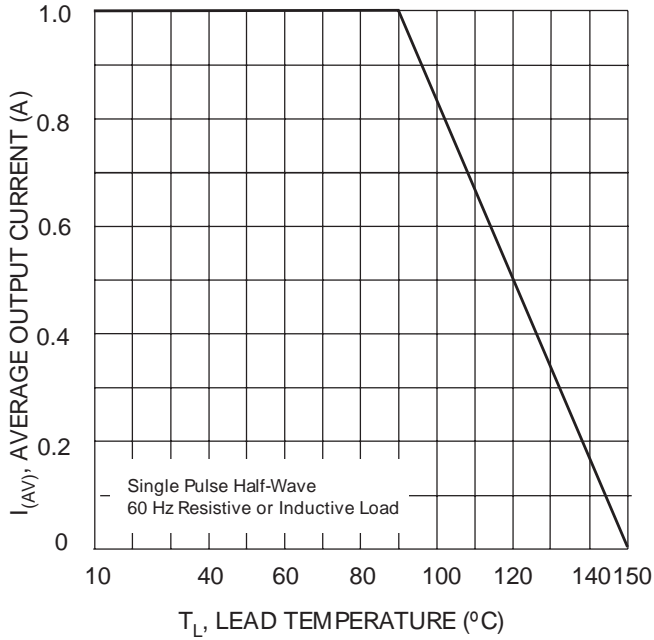


Fig. 1 Forward Current Derating Curve

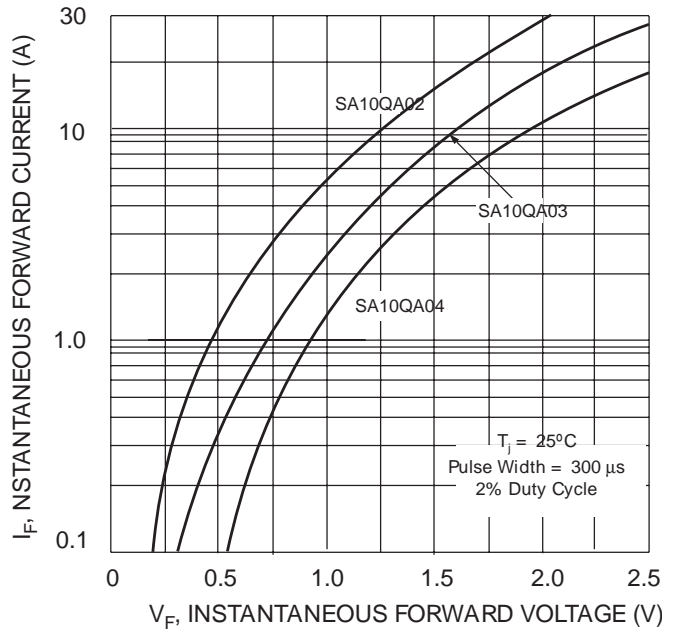


Fig. 2 Typical Forward Characteristics

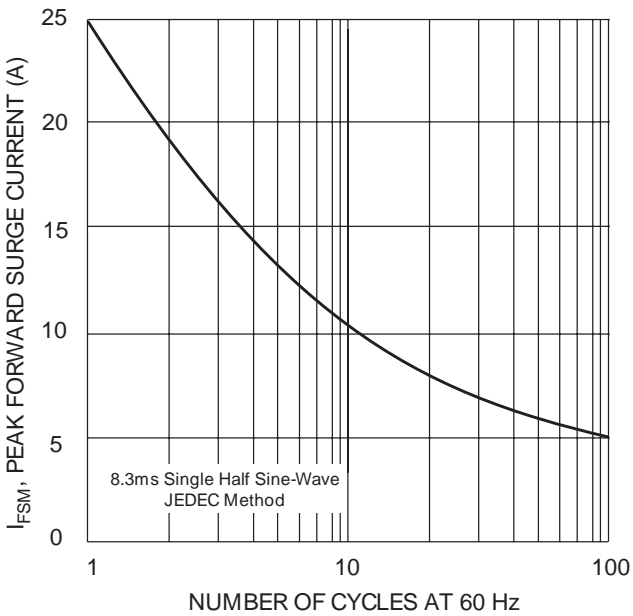


Fig. 3 Maximum Non-Repetitive Peak Fwd Surge Current

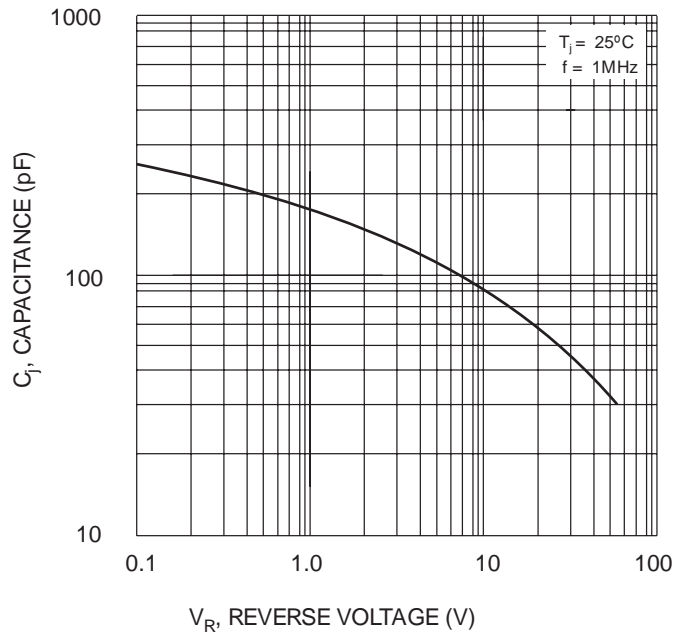


Fig. 4 Typical Junction Capacitance