

**VOLTAGE RANGE: 50 - 1000V**

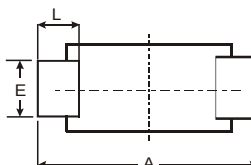
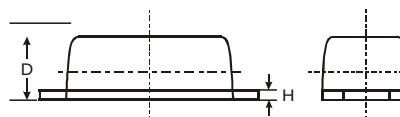
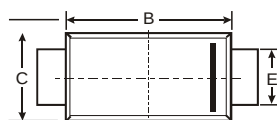
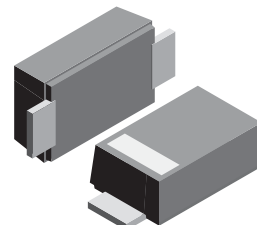
**CURRENT: 1.0 A**

### Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop
- Low Power Loss
- Built-in Strain Relief
- Plastic Case Material has UL Flammability Classification Rating 94V-O

### Mechanical Data

- Case: JEDEC SOD-123FL molded plastic body over passivated chip
- Terminals : Plated axial leads, solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



| SOD-123FL            |      |      |       |
|----------------------|------|------|-------|
| Dim                  | Min  | Max  | Typ   |
| A                    | 3.50 | 3.80 | 3.65  |
| B                    | 2.60 | 2.90 | 2.75  |
| C                    | 1.70 | 1.90 | 1.80  |
| D                    | 0.09 | 1.10 | 1.00  |
| E                    | 0.08 | 1.10 | 0.095 |
| H                    | 0.12 | 0.20 | 0.16  |
| L                    | 0.07 | 0.09 | 0.08  |
| All Dimensions in mm |      |      |       |

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic                                                                                                     | Symbol                                                 | SM4001FL    | SM4002FL | SM4003FL | SM4004FL | SM4005FL | SM4006FL | SM4007FL | Unit |
|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------|-------------|----------|----------|----------|----------|----------|----------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage                             | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>R</sub> | 50          | 100      | 200      | 400      | 600      | 800      | 1000     | V    |
| RMS Reverse Voltage                                                                                                | V <sub>R(RMS)</sub>                                    | 35          | 70       | 140      | 280      | 420      | 560      | 700      | V    |
| Average Rectified Output Current @T <sub>L</sub> = 100°C                                                           | I <sub>O</sub>                                         | 1.0         |          |          |          |          |          |          | A    |
| Non-Repetitive Peak Forward Surge Current<br>8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | I <sub>FSM</sub>                                       | 30          |          |          |          |          |          |          | A    |
| Forward Voltage @I <sub>F</sub> = 1.0A                                                                             | V <sub>FM</sub>                                        | 1.10        |          |          |          |          |          |          | V    |
| Peak Reverse Current @T <sub>A</sub> = 25°C<br>At Rated DC Blocking Voltage @T <sub>A</sub> = 125°C                | I <sub>RM</sub>                                        | 5.0<br>200  |          |          |          |          |          |          | μA   |
| Reverse Recovery Time (Note 1)                                                                                     | t <sub>rr</sub>                                        | 2.5         |          |          |          |          |          |          | μs   |
| Typical Junction Capacitance (Note 2)                                                                              | C <sub>j</sub>                                         | 15          |          |          |          |          |          |          | pF   |
| Typical Thermal Resistance (Note 3)                                                                                | R <sub>θJL</sub>                                       | 30          |          |          |          |          |          |          | K/W  |
| Operating and Storage Temperature Range                                                                            | T <sub>j</sub> , T <sub>STG</sub>                      | -65 to +175 |          |          |          |          |          |          | °C   |

Note: 1. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>rr</sub> = 0.25A,  
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.  
 3. Mounted on P.C. Board with 8.0mm<sup>2</sup> land area.

FIG. 1 - FORWARD CURRENT DERATING CURVE

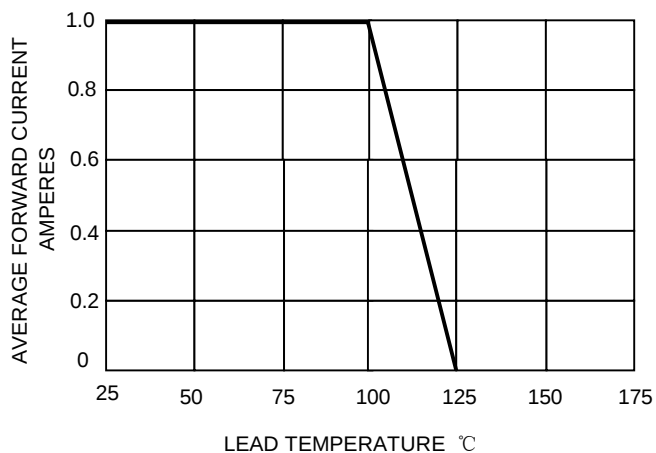


FIG.2 - MAXIMUM NON-REPETITIVE SURGE CURRENT

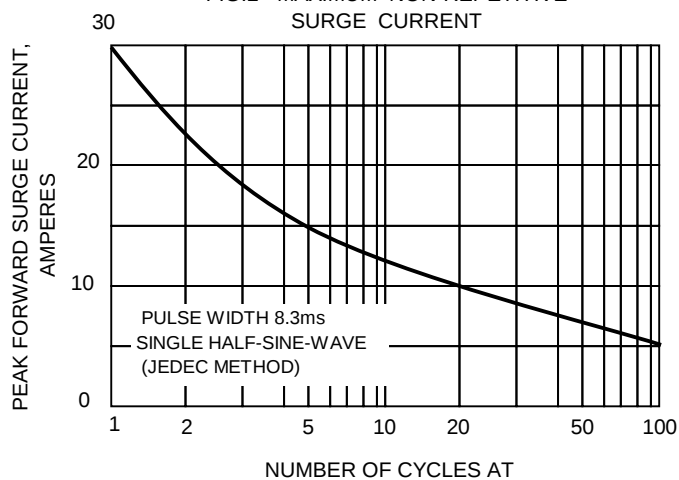


FIG.3-TYPICAL FORWARD CHARACTERISTICS

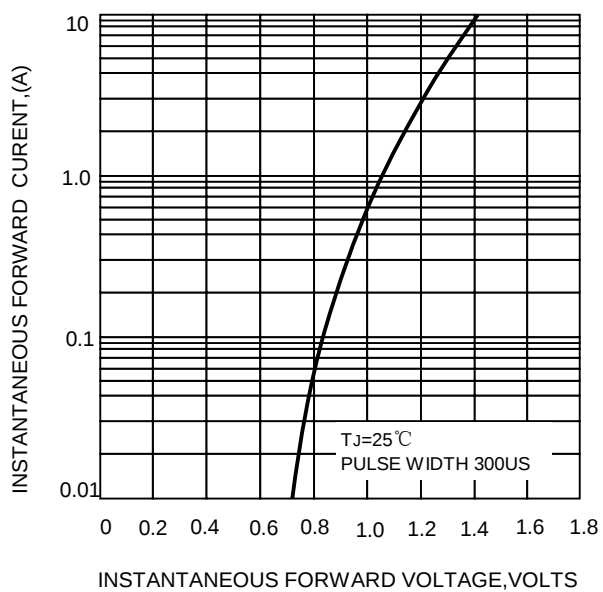


FIG.4-TYPICAL REVERSE CHARACTERISTICS

