

VOLTAGE RANGE: 20 - 60V

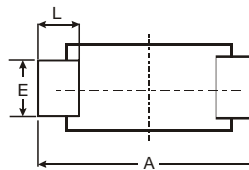
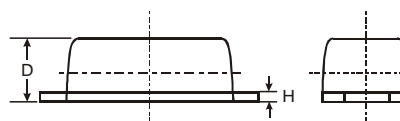
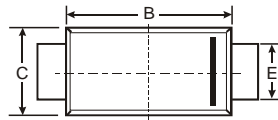
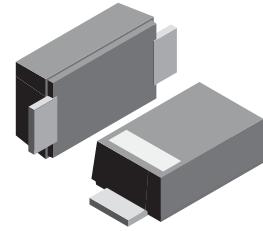
CURRENT: 1.0 A

Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

Mechanical Data

- Case: SOD-123FL
plastic body over passivated junction
- Terminals : Plated axial leads,
- solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.58	3.72	3.65
B	2.72	2.78	2.75
C	1.77	1.83	1.80
D	1.02	1.08	1.05
E	0.097	1.03	1.00
H	0.13	0.17	0.15
L	0.53	0.57	0.55
All Dimensions in mm			

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	DFLS120	DFLS130	DFLS140	DFLS150	DFLS160	Unit
Peak Repetitive Reverse Voltage	V _{RRM}						
Working Peak Reverse Voltage	V _{RWM}	20	30	40	50	60	V
DC Blocking Voltage	V _R						
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	35	42	V
Average Rectified Output Current @T _L = 75°C	I _O	1.0					A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	30					A
Forward Voltage @I _F = 1.0A	V _{FM}	0.50			0.70		V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	0.5 20					mA
Typical Thermal Resistance (Note 1)	R _{θJL} R _{θJA}	28 88					°C/W
Operating Temperature Range	T _j	-65 to +125					°C
Storage Temperature Range	T _{STG}	-65 to +150					°C

Note: 1. Mounted on P.C. Board with 5.0mm² copper pad area.

RATINGS AND CHARACTERISTIC CURVES DFLS120 THRU DFLS160

